

| | Op | Reg Dst | Branch | Mem Read | Mem ToReg | ALUop (2) | Mem Write | ALU src | Reg Write |
|--------|--------|---------|--------|----------|-----------|-----------|-----------|---------|-----------|
| r-type | 000000 | | | | | | | | |
| beq | 000100 | | | | | | | | |
| addi | 001000 | | | | | | | | |
| slti | 001010 | | | | | | | | |
| lw | 100011 | | | | | | | | |
| sw | 101011 | | | | | | | | |

| | | ALUop (2) | Funct | Operation (4) |
|------------|-----|-----------|---------|---------------|
| addi,lw,sw | add | 00 | xxxxxxx | |
| beq | sub | 01 | xxxxxxx | |
| add | add | 10 | 100000 | |
| sub | sub | 10 | 100010 | |
| and | and | 10 | 100100 | |
| or | or | 10 | 100101 | |
| nor | nor | 10 | 100111 | |
| slt | slt | 10 | 101010 | |
| slti | slt | | xxxxxxx | |